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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/772,820	02/05/2004	Andy T. Nguyen	X-1493 US	5970
. 24309 75	590 10/06/2005		EXAM	INER
XILINX, INC	DEPARTMENT		TON, MY	TRANG
2100 LOGIC D			ART UNIT	PAPER NUMBER
SAN JOSE, CA			2816	

DATE MAILED: 10/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Application No. 10772.820 NGUVEN, ANDY T.  Examiner Art Unit Prizery Name (Period for Reply Art Unit Prizery Name). The MAILING DATE of this communication appears on the cover sheet with the correspondence address → Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  Extensions of time may be available under the provides of 32 FCR 1.136(a). In a event, however, may a reply tea limiter the distribution of the communication of the state of the communication
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application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
MY-TRANG NUTON
PRIMARY EXAMINER
Attachment(s)
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) 2) Paper No(s)/Mail Date
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  Paper No(s)/Mail Date 2/5/04.  5) Notice of Informal Patent Application (PTO-152)  6) Other:

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Art Unit: 2816

## **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-9 and 15-23 are rejected under 35 U.S.C. 102(b) as being anticipated by Deas et al (U.S Patent No. 6,642,764).

Deas et al disclose in fig. 1 a high precision receiver with skew compensation including:

first and second multi-bit circuit input terminals (Vinput, Vref);

a comparator circuit (10) having a first multi-bit input terminal (15), a second multi-bit input terminal (13) coupled to the second circuit input terminal (Vinput), and an output terminal (11);

a multiplexer circuit (23) having a first data input terminal (26) coupled to the first circuit input terminal (Vref), a second data input terminal (27), a select terminal (28) coupled to the output terminal (11) of the comparator circuit (10), and an output terminal (31) coupled to the first input terminal (15) of the comparator circuit (10); and

a first adder circuit (22) coupled between the first circuit input terminal (Vref) and the second data input terminal of the multiplexer circuit (23) as recited in claim 1.

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Regarding claim 2: a second adder circuit (inherent seen as 24-, inside 22) coupled between the first circuit input terminal (Vref) and the first data input terminal of the multiplexer circuit (23).

Regarding claim 3: the first adder circuit comprises a subtractor (22).

Regarding claim 4. the first adder circuit comprises an adder (22).

Regarding claim 5: a first overflow prevention circuit (30) coupled between the output terminal (11) of the comparator circuit (10) and the select terminal (28) of the multiplexer circuit (23), the first overflow prevention circuit (30) having an additional input terminal coupled to the first circuit input terminal (inherent seen coupled to Vref via 22, 23, 10).

Regarding the limitation recited in claims 6-9: because the claimed structure is fully met by Deas et al, thus, the recited function or result limitations for the comparator circuit recited in claims 6-9 will necessarily be inherent in Deas et al, as held by the court in In re Best, 195 USPQ 430.

The method recited in claims 15-23 are inherent to the operation of the high precision receiver circuit of the Deas et al.

## Allowable Subject Matter

Claims 10-14 are allowable over the prior art of record. None of the prior art disclosed or suggested to show the particular structure and/or the particular operation recited in these claims namely: the combination of "a counter circuit", "first – second subtractors", "first – second multiplexers", "a comparator circuit" and "a phase shifter" as recited in claim 10.

Art Unit: 2816

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

My-Trang N. Ton Primary Examiner Art Unit 2816

September 30, 2005